

(11) **Publication No.: 2 313 708**

(21) **No. 76 12194**

(54) **Circuit and method for controlling the duty factor of a pulse-modulated signal.**

(51) International classification (Int. Cl.<sup>2</sup>). **G 05 F 1/08; A 61 N 3/04.**

(22) Filing date April 23, 1976, at 4:25 p.m.

(33) (32) (31) Priority claimed: *Patent application filed in the United States on June 2, 1975, No. 582,593 in the names of Stephen William Andrews and Stanley Woltoz.*

(41) Date the application was made available to the public: B.O.P.I. – “Listes” No. 53 of 12/31/1976.

(71) Applicant: SYBRON CORPORATION, USA.

(73) Holder: *same as (71)*

(74) Agent: Simonnot, Rinuy, Santarelli.

#### CLAIM 1

Circuit for controlling the duty factor of a pulse-modulated output signal and applied to several electrodes intended to come into contact with a patient, this circuit, which is intended for an electrosurgery device, being characterized in that it comprises a component for sampling the voltage appearing between the electrodes, a threshold component producing a signal when the voltage amplitude between the electrodes exceeds a pre-set value and a modulator intended to give said output signal a first duty factor in the absence of said threshold signal and a second duty factor in the presence of this threshold signal.